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Application Number	10/536710	1
Filing Date	10/536719 Herewith	
First Named Inventor	Horotaka OCHI	
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HAPPIN .

27 MAY 2005 10/534719

As a below named inventor, I hereby declare that:

my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural names are named below) of the invention entitled: ADAPTIVE EQUALIZATION CIRCUIT AND ADAPTIVE EQUALIZATION METHOD described and claimed in the international application number PCT/JP03/15378 filed December 2, 2003 (if any), the specification and claims of which I have reviewed and understand and as amended on and for which I solicit a patent. I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a), and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to my international application by me or my legal representatives or assigns, except as follows: Japanese Patent Application No. 2002-349683 filed on December 2, 2002 The priority of the above applications (if any), filed within a year prior to my international application is hereby claimed under 35 USC 119. I hereby appoint the following as my attorneys of record with full power of substitution and revocation to prosecute this application and to transact all business in the patent office: Roger W. Parkhurst, Reg. No. 25,177; Charles A. Wendel, Reg. No. 24,453; Lawrence D. Eisen, Reg. No. 41,009. ALL CORRESPONDENCE IN CONNECTION WITH THIS APPLICATION SHOULD BE SENT TO: PARKHURST & WENDEL, L.L.P., 1421 PRINCE STREET, SUITE 210, ALEXANDRIA, VIRGINIA 22314-2805, TELEPHONE (703) 739-0220. I hereby declare that I have reviewed and understand the contents of this Declaration, and that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon. Full Name of Sole Hirotaka or First Inventor . Middle Initial Family Name Given Name . . Ochi Hirotaka Inventor's Signature 27, 2005 April Date of Signature Month Dav Year 577-1-105, Kanbaikou, Saijyo-shi, Ehime Residence 6. State or Province Country City Citizenship Japan 7. c/o MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD. 8. Post Office address (Insert complete mailing 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501 Japan address, including country)

*IF THERE IS MORE THAN ONE INVENTOR USE PAGE 2 AND PLACE AN "X" HERE□.